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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/022,854	12/20/2001	Seung Kuk Ahn	049128-5030	1440

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EXAMINER

LAO, LUN YI

ART UNIT PAPER NUMBER

2677

DATE MAILED: 09/23/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/022,854

Applicant(s)

AHN, SEUNG KUK

Examiner

LUN-YI LAO

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 06 July 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-4 is/are rejected.
- 7) ☒ Claim(s) 6 and 7 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
- 1) ☒ Certified copies of the priority documents have been received.
 - 2) ☐ Certified copies of the priority documents have been received in Application No. _____.
 - 3) ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>7/6/2005</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Objections

1. Claims 1-7 are objected to because of the following informalities:

The recitation of "the reset signal is fed back and connected to the source shift clock" in claims 1 and 3 should be changed to "the reset signal is connected to the toggle clock and reset circuit for generating said source shift clock(see figures 6-7).

Appropriate correction is required.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1 and 3 are rejected under 35 U.S.C. 102(b) as being anticipated by Onda(6,084,562).

As to claims 1 and 3, Onda teaches an LCD display comprising the steps of: receiving a data enable signal(Vs) for indicating a time interval when a video data exists; detecting an enable initiation time of the data enable signal(Vs); generating a reset signal(VH) at the enable initiation time of data signal(Vs); resetting a source shift

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clock signal(CPH) for sampling video data(Vs') in response to the reset signal(VH) and the reset signal connected to a timing generating circuit(71) for generating a source shift clock signal(CPH)(see figures 1-5; column 5, lines 24-56 and column 6, lines 52-64).

4. Claims 1 and 3 are rejected under 35 U.S.C. 102(b) as being anticipated by Eto et al(5,301,031).

As to claims 1 and 3, Eto et al teach an LCD display comprising the steps of: receiving a data enable signal(1, R) for indicating a time interval when a video data exists; detecting an enable initiation time of the data enable signal(1, R); generating a reset signal at the enable initiation time of data signal(1, R); resetting a source shift clock signal for sampling video data(1, R) in response to the reset signal and the reset signal connected to a timing control circuit(3) for generating a source shift clock signal(CKH)(see figures 1-2; column 3, lines 1-50 and column 4, lines 4-12).

5. Claims 2 and 4-5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Onda or Eto et al in view of Sekido et al(5,999,158).

Onda or Eto et al fail to disclose a source driving circuit for latching video signal after sampling.

Sikido et al teach a source driving circuit having a latching circuit(4) for latching sampling video signal(see figure 4; column 3, lines 61-68 and column 4, lines 1-4). It would have been obvious to have modified Onda or Eto et al with the teaching of Shiki, so as to transfer sampling data signal to an LCD display panel(see column 4, lines 2-4).

Response to Arguments

6. Applicant's arguments with respect to claims 1-6 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Ozawa(6,683,596) teaches an LCD display comprising a selection control signal(SS) synchronizing with the data signal(D").

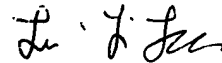
8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lun-yi Lao whose telephone number is 571-272-7671. The examiner can normally be reached on M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on 571-272-7681. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

September 20, 2005

A handwritten signature in black ink, appearing to read 'Lun-yi Lao', is positioned above the printed name.

Lun-yi Lao
Primary Examiner